UTILITY PATENT APPLICATION **TRANSMITTAL**

ply for new nonprovisional applications under 37 CFR 1.53(b)

2004 0156A Attorney Docket No.:

First Named Inventor: Tomoo KIMURA et al.

Title: LOGIC CIRCUIT OPTIMIZING METHOD, LOGIC CIRCUIT OPTIMIZING DEVICE AND LOGIC CIRCUIT COMPOSING DEVICE

Express Mail Label No.:

APPLICATION ELEMENTS

See MPEP chapter 600 concerning utility patent application contents.

1. [X] Fee Transmittal Form (Submit an original, and a duplicate for fee processing)

- 2. [] Small Entity Status is hereby asserted.
- 3. [X] Specification

[Total Pages: 37]

(preferred arrangement set forth below)

- Descriptive title of the Invention
- Cross References to Related Applications
- Statement Regarding Fed sponsored R & D
- Reference to Sequence Listing, a table, or a computer program listing appendix.
- Background of the Invention
- Brief Summary of the Invention
- Brief Description of the Drawings (if filed)
- **Detailed Description**
- Claim(s)
- Abstract of the Disclosure

[Total Sheets: 17]

[Total Pages: 3]

- 5. [X] Oath or Declaration
 - a.1. [] Newly executed (original or copy)
 - a.2. [X] Unexecuted

4. [X] Drawing(s) (35 USC 113)

- [] Copy from a prior application (37 CFR 1.63(d)) (for continuation/divisional with Box 18 completed)
 - i. [] DELETION OF INVENTOR(S)

Signed statement attached deleting inventor(s) named in the prior application, see 37 CFR 1.63(d)(2) and 1.33(b).

6. [] Application Data Sheet (see 37 CFR 1.76)

Commissioner for Patents

ADDRESS TO:

P.O. Box 1450

Alexandria, VA 22313-1450

7. [] CD-ROM or CD-R in duplicate, large table or computer program (Appendix)

8. [] Nucleotide and/or Amino Acid Sequence Submission (if applicable, all necessary)

- a. [] Computer Readable Form
- b. Specification Sequence Listing on:
 - i. [] CD-ROM or CD-R (2 copies); or
 - ii. [] Paper
- c. [] The paper and computer readable copies are identical

ACCOMPANYING APPLICATION PARTS

- [] Assignment Papers (cover sheet & document(s))
- [] 37 CFR 3.73(b) Statement (when there is an assignee) [] Power of Attorney
- [] English Translation Document (if applicable)
 - [] Information Disclosure Statement (IDS)/PTO-1449
 - [] Copies of IDS Citations
- 13. [] Preliminary Amendment
- 14. [X] Return Receipt Postcard (MPEP 503) (Should be specifically itemized)
- Certified Copy of Priority Document(s) 15. [X]
 - (if foreign priority is claimed)
- 16. [] Request and Certification under 35 U.S.C. 122 (b)(2)(B)(i). Applicant must attach form PTO/SB/35 or its equivalent.
- 17. [X] Other 1. Cover Letter for Application Filed Without Executed Declaration
 - 2. Claim of Priority Under 35 USC 119

18. If a CONTINUING APPLICATION, check appropriate box, and supply the requisite information below, and in a preliminary amendment, or 🔞 in an Application Data Sheet:

[] Continuation

[] Divisional

[] Continuation-in-part (CIP)

of prior application No.

Prior Application Information: Examiner

Group Art Unit

For CONTINUATION OR DIVISIONAL APPS only: The entire disclosure of the prior application, from which an oath or declaration is supplied under Box 5b, is considered a part of the disclosure of the accompanying continuation or divisional application and is hereby incorporated by reference therein. The incorporation <u>can only</u> be relied upon when a portion has been inadvertently omitted from the submitted application parts.

19. CORRESPONDENCE ADDRESS

CUSTOMER NO.

000513

Jeffrey X. Filipek Registration No. 41,471

> WENDEROTH, LIND & PONACK, L.L.P. 2033 "K" Street, N.W., Suite 800 Washington, D.C. 20006-1021 Phone: (202) 721-8200 Fax:(202) 721-8250

> > February 9, 2004

THE COMMISSIONER IS AUTHORIZED TO CHARGE ANY DEFICIENCY IN THE FEES FOR THIS PAPER TO DEPOSIT ACCOUNT NO. 23-0975

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Serial No. NEW

:

Tomoo KIMURA et al.

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Attn: APPLICATION BRANCH

Filed February 9, 2004

Attorney Docket No. 2004_0156A

LOGIC CIRCUIT OPTIMIZING METHOD, LOGIC CIRCUIT OPTIMIZING DEVICE AND LOGIC CIRCUIT COMPOSING DEVICE

PATENT OFFICE FEE TRANSMITTAL FORM

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 THE COMMISSIONER IS AUTHORIZED TO CHARGE ANY DEFICIENCY IN THE FEES FOR THIS PAPER TO DEPOSIT ACCOUNT NO. 23-0975

Sir:

Attached hereto is a check in the amount of \$1064.00 to cover Patent Office fees relating to filing the following attached papers:

New application \$770.00

Additional Claims Fee:

 Excess of Twenty
 \$ 36.00

 Independent
 \$258.00

A duplicate copy of this paper is being submitted for use in the Accounting Division, Office of Finance.

The Commissioner is authorized to charge any deficiency or to credit any overpayment associated with this communication to Deposit Account No. 23-0975, with the EXCEPTION of deficiencies in fees for multiple dependent claims in new applications.

Respectfully submitted,

Tomoo KIMURA et

Bv≖

ffrey R. Filipek

Registration No. 41,471 Attorney for Applicants

JRF/fs WENDEROTH, LIND & PONACK, L.L.P. 2033 K St., N.W., Suite 800 Washington, D.C. 20006-1021 Telephone (202) 721-8200 February 9, 2004

[Check No. 6010]

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Tomoo KIMURA et al. : Attn: APPLICATION BRANCH

Serial No. NEW : Attorney Docket No. 2004 0156A

Filed February 9, 2004

LOGIC CIRCUIT OPTIMIZING METHOD, LOGIC CIRCUIT OPTIMIZING DEVICE AND LOGIC CIRCUIT COMPOSING

DEVICE

COVER LETTER FOR APPLICATION FILED WITHOUT EXECUTED DECLARATION

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

THE COMMISSIONER IS AUTHORIZED TO CHARGE ANY DEFICIENCY IN THE FEES FOR THIS PAPER TO DEPOSIT ACCOUNT NO. 23-0975

Sir:

The above-identified application has been submitted <u>without</u> an executed oath or declaration pursuant to 37 CFR 1.41(c).

It is respectfully requested that this application be assigned a serial number and awarded a filing date pursuant to 37 CFR 1.53.

A duly executed oath or declaration pursuant to 37 CFR 1.63 will be submitted after notification by the U.S. Patent and Trademark Office pursuant to 37 CFR 1.52(d).

A non-executed copy of the Declaration and Power of Attorney, containing the inventorship information, is attached. It is respectfully requested that all communications be directed to the firm indicated on the attached Declaration and Power of Attorney, namely:

WENDEROTH, LIND & PONACK, L.L.P. 2033 K Street, N.W., Suite 800 Washington, D.C. 20006-1021

The required U.S. Patent and Trademark Office Filing Fee is submitted herewith.

Respectfully submitted,

Tomoo KIMURA et al.

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Registration No. 41,471 Attorney for Applicants

JRF/fs Washington, D.C. 20006-1021 Telephone (202) 721-8200 Facsimile (202) 721-8250 February 9, 2004